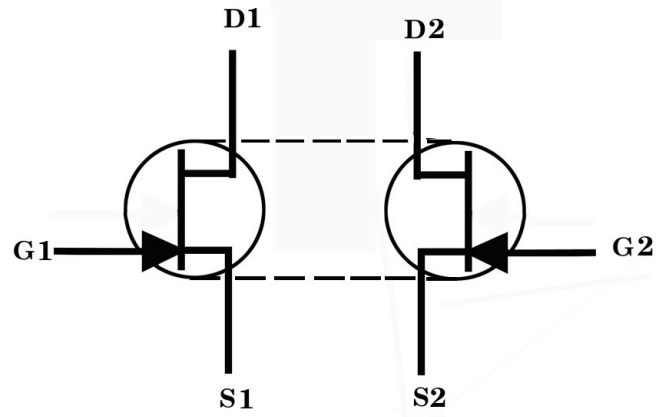


# Toshiba

## 2SK109 Transistors

### Dual N-Channel JFET



Military/High-Rel N

$V_{(BR)DSS}$  (V)

$V_{(BR)GSS}$  (V) 50

$I_{(D)}$  Max. (A) 20m

$I_{(G)}$  Max. (A)

Absolute Max. Power Diss. (W) 150m

Maximum Operating Temp ( $^{\circ}$ C) 125 $\bar{5}$

$I_{(GSS)}$  Max. (A) 1.0n

@ $V_{(GS)}$  (V) (Test Condition)

$V_{(GS)off}$  Min. (V)

$I_{(DSS)}$  Max. (A)

$V_{(GS)off}$  Max. (V) 3.0

@ $V_{gs}$  (test) (V)

@ $V_{(DS)}$  (V) (Test Condition)

@ $I_{(D)}$  (A) (Test Condition)

$I_{(DSS)}$  Min. (A) 4.0m

$I_{(DSS)}$  Max. (A)

@ $V_{(DS)}$  (V) (Test Condition)

$r_{(DS)on}$  Max. (Ohms)

$g_{(fs)}$  Min. (S) Trans. conduct. 6.0m

$g_{(fs)}$  Max. (S) Trans. conduct. :

